



- HPCS: High Productivity Computing Systems
 - Goal: Raise productivity by 10x for the year 2010
 - Productivity = Performance
 - + Programmability
 - + Portability
 - + Robustness
- Phase II: Cray, IBM, Sun (July 2003 June 2006)
 - Evaluation of the entire system architecture's impact on productivity...
 - processors, memory, network, I/O, OS, runtime, compilers, tools, ...
 - ...and new languages:
 - IBM: X10

Sun: Fortress

Cray: Chapel

- Phase III: Cray, IBM (July 2006 2010)
 - Implement the systems and technologies resulting from phase II

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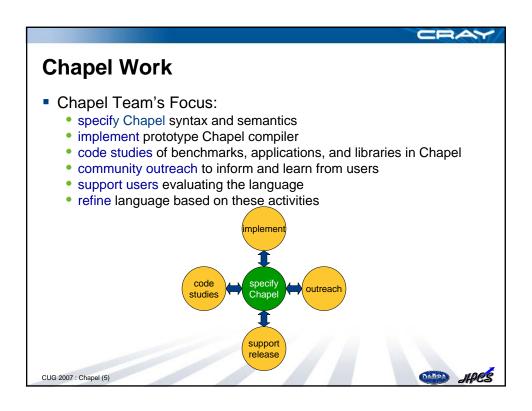
Chapel and Productivity

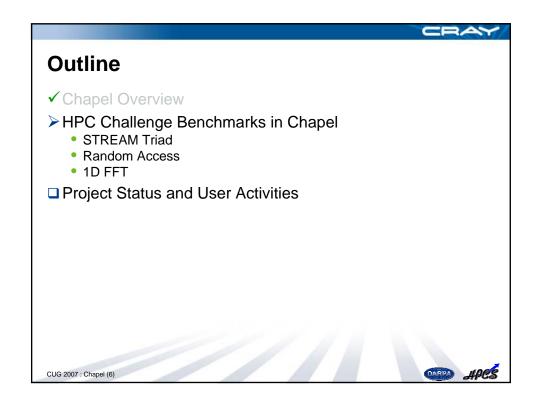
- Chapel's Productivity Goals:
 - vastly improve programmability over current languages/models
 - writing parallel codes
 - reading, modifying, maintaining, tuning them
 - support performance at least as good as MPI
 - competitive with MPI on generic clusters
 - better than MPI on more productive architectures like Cray's
 - improve portability compared to current languages/models
 - as ubiquitous as MPI, but with fewer architectural assumptions
 - more portable than OpenMP, UPC, CAF, ...
 - improve code robustness via improved semantics and concepts
 - eliminate common error cases altogether
 - better abstractions to help avoid other errors

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HPC Challenge Overview

Motivation: Growing realization that top-500 often fails to reflect practical/sustained performance

- measured using HPL, which essentially measures peak FLOP rate
- user applications often constrained by memory, network, ...

HPC Challenge (HPCC):

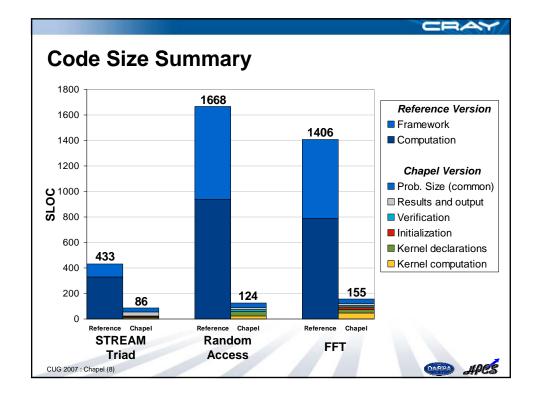
- suite of 7 benchmarks to measure various system characteristics
- annual competition based on 4 of the HPCC benchmarks
 - class 1: best performance (award per benchmark)
 - class 2: most productive
 - 50% performance
 - 50% code elegance, size, clarity

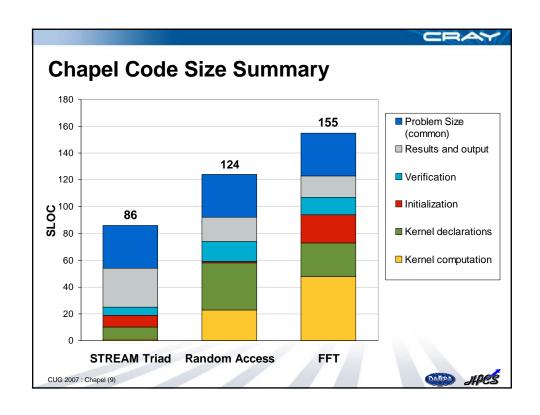
For more information:

- HPCC Benchmarks: http://icl.cs.utk.edu/hpcc/
- HPCC Competition: http://www.hpcchallenge.org

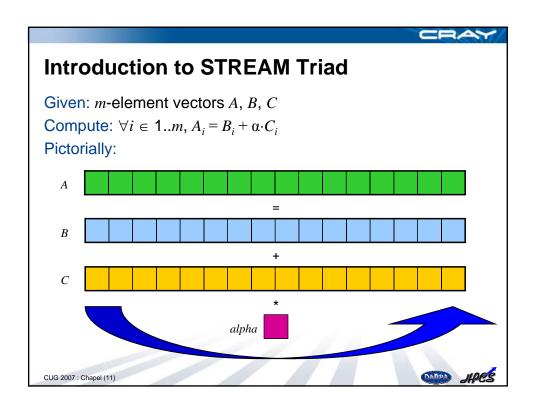
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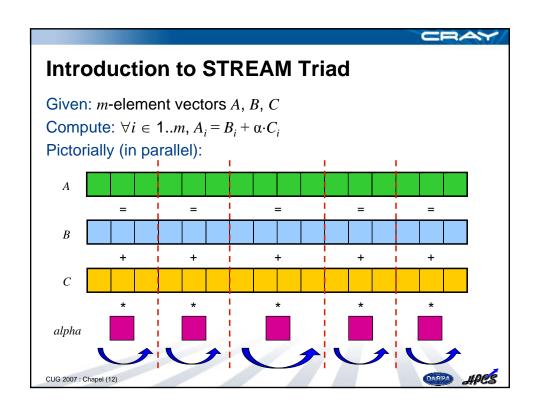


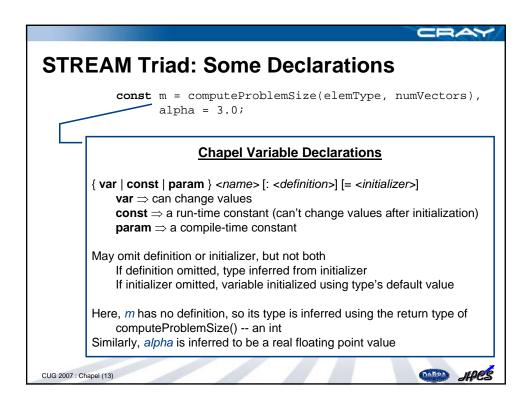


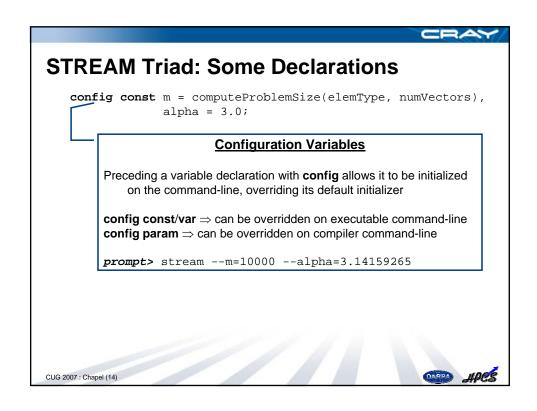


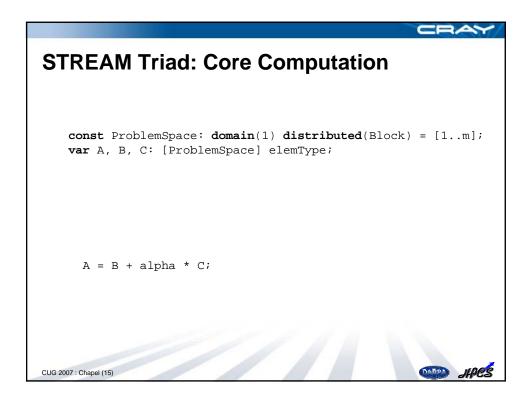


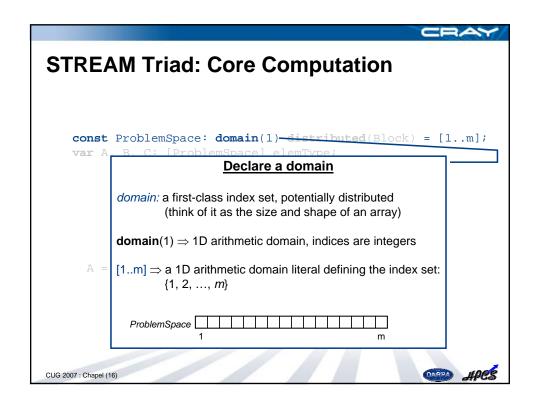


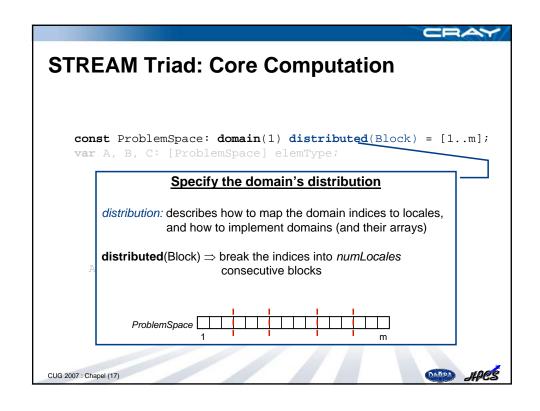


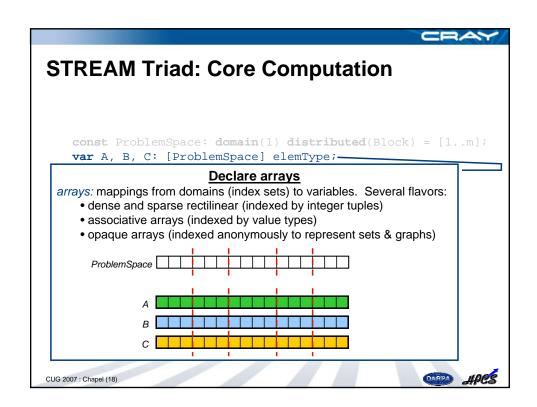


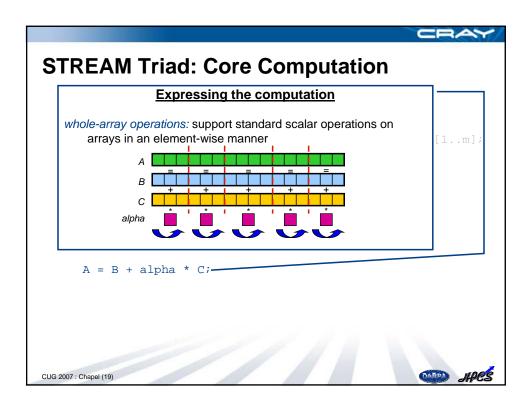












```
STREAM Triad: complete main() routine

def main() {
    printConfiguration();

    const ProblemSpace: domain(1) distributed(Block) = [1..m];
    var A, B, C: [ProblemSpace] elemType;

    initVectors(B, C);

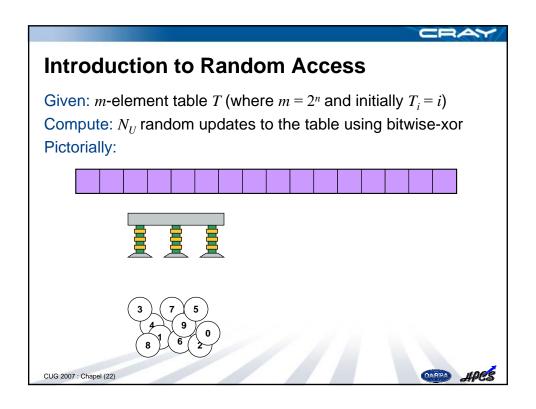
    var execTime: [1..numTrials] real;

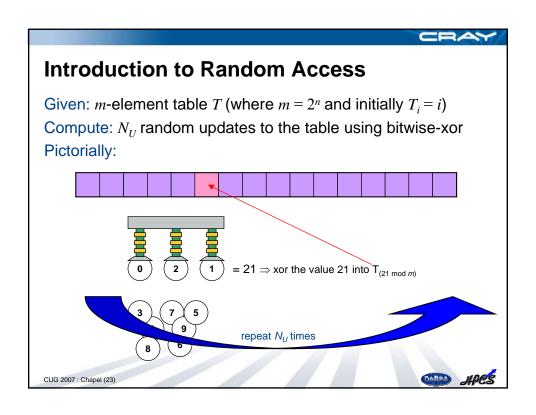
    for trial in 1..numTrials {
        const startTime = getCurrentTime();
        A = B + alpha * C;
        execTime(trial) = getCurrentTime() - startTime;
    }

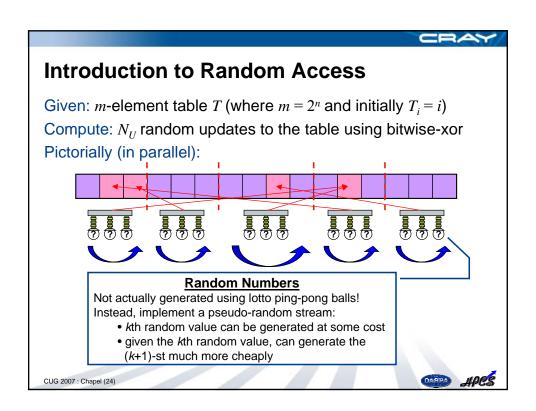
    const validAnswer = verifyResults(A, B, C);
    printResults(validAnswer, execTime);
}

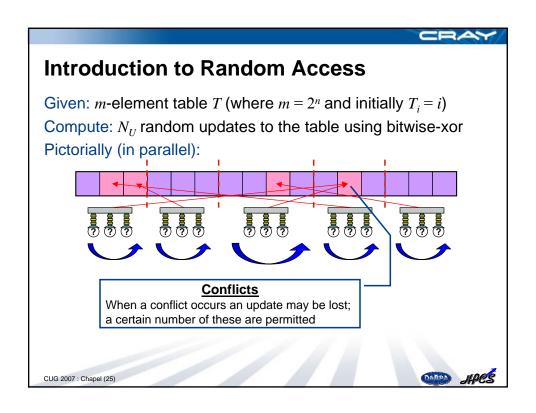
customain triangle (20)
```

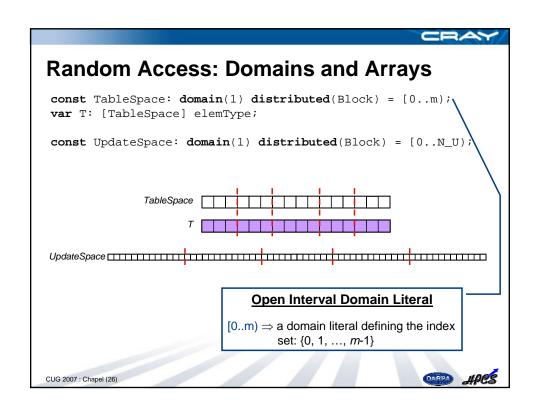










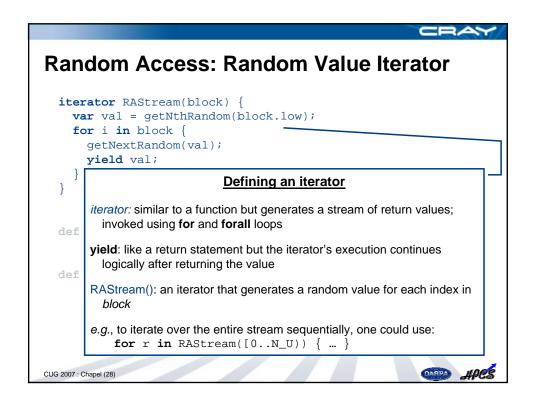


```
Random Access: Random Value Iterator

iterator RAStream(block) {
  var val = getNthRandom(block.low);
  for i in block {
    getNextRandom(val);
    yield val;
  }
}

def getNthRandom(in n) { ... }

def getNextRandom(inout x) { ... }
```

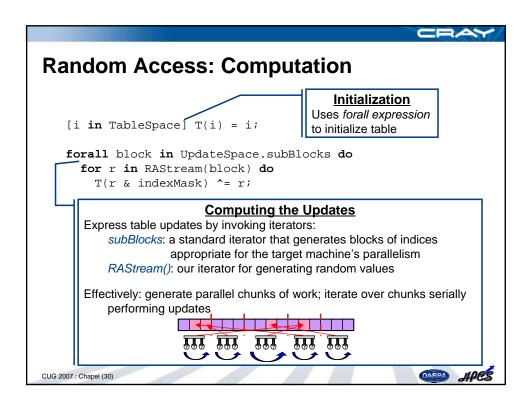


```
Random Access: Computation

[i in TableSpace] T(i) = i;

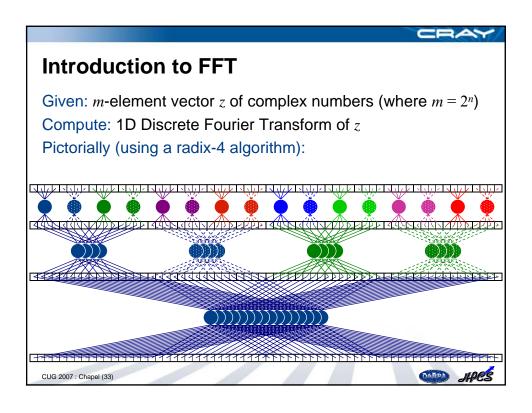
forall block in UpdateSpace.subBlocks do
   for r in RAStream(block) do
    T(r & indexMask) ^= r;

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```



Random Access: Adding Determinism [i in TableSpace] T(i) = i; forall block in UpdateSpace.subBlocks do for r in RAStream(block) do atomic T(r & indexMask) ^= r; Ensuring Determinism (e.g., for Verification) atomic: indicates that code executes atomically from other threads' viewpoints For a case like this, could be implemented using... ...Atomic Memory Operations (AMOs) ...Full/Empty bits ...Compare-and-Swap ...Locks More generally, atomics require transactional memory concepts (SW or HW)





```
FFT: Computation

for i in [2..log2(numElements)) by 2 {
    const m = radix*span, m2 = 2*m;

    forall (k,kl) in (Adom by m2, 0..) {
        var wk2 = ..., wk1 = ..., wk3 = ...;

        forall j in [k..k+span) do
            butterfly(wk1, wk2, wk3, A[j..j+3*span by span]);

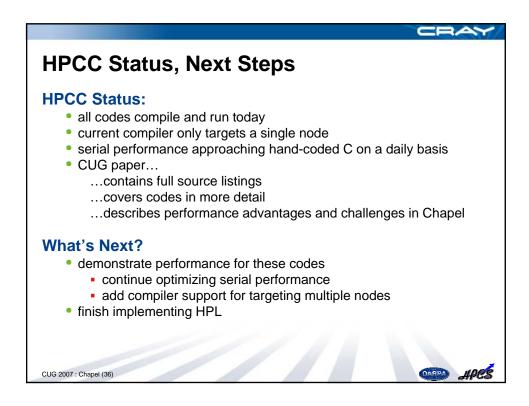
        wk1 = ...; wk3 = ...; wk2 *= 1.0i;

        forall j in [k+m..k+m+span) do
            butterfly(wk1, wk2, wk3, A[j..j+3*span by span]);
        }
        span *= radix;
    }

    def butterfly(wk1, wk2, wk3, inout A:[1..radix]) { ... }

    cug 2007: Chapel (34)
```

```
FFT: Computation
                                           Sequential loop to express
                                           phases of computation
   for i in [2..log2(numElements)) by 2 {
      const m = radix*span, m2 = 2*m;
     -forall (k,k1) in (Adom by m2, 0..) {
        var wk2 = ..., wk1 = ..., wk3 = ...;
                                            Nested forall loops to express
                                          a phase's parallel butterflies
        forall j in [k..k+span) do
          butterfly(wk1, wk2, wk3, A[j..j+3*span by span]);
                                             Support for complex and
        wk1 = ...; wk3 = ...; wk2 *= 1.0i;
                                            imaginary types simplifies math
        forall j in [k+m..k+m+span) do
          butterfly(wk1, wk2, wk3, A[j..j+3*span by span]);
                                     Generic arguments allow butterfly() to be
      span *= radix;
                                     called with complex, real, or imaginary
                                     twiddle factors
   def butterfly(wk1, wk2, wk3, inout A:[1..radix]) { ... }
CUG 2007 : Chapel (35)
```





- Chapel supports HPCC codes attractively
 - · clear, concise, general
 - parallelism expressed in architecturally-neutral way
 - benefit from Chapel's global-view parallelism
 - utilizes generic programming and modern SW Engineering principles
 - should serve as an excellent reference for future HPCC competitors
- Note that HPCC benchmarks are relatively simple
 - all data structures are 1D vectors
 - locality very data driven
 - minimal task- & nested parallelism
 - little need for OOP, abstraction
- ...HPCC designed to stress systems, not languages
 - would like to see similar competitions emerge for richer computations

CUG 2007 : Chapel (37)







- √ Chapel Overview
- √ HPC Challenge Benchmarks in Chapel
 - ✓ STREAM Triad
 - ✓ Random Access
 - ✓ 1D FFT
- Project Status and User Activities

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Project Status, Next Steps

- Chapel specification:
 - revised draft language specification available on Chapel website
 - editing to add additional examples & rationale; improve clarity
- Compiler implementation:
 - improving serial performance
 - starting on parallel implementation
 - · adding missing serial features
- Code studies:
 - NAS Parallel Benchmarks: CG (well underway), IS, FT, MG
 - Linear Algebra routines: block LU, block Cholesky, matrix mult.
 - Other applications of interest: Fast Multipole Method, SSCA2, ...
- Release:
 - made a preliminary release to government team December 2006
 - initial response from those users has been positive, encouraging
 - next release due Summer 2007.

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- Two main efforts to date, both at ORNL:
 - · Robert Harrison, Wael Elwasif, David Bernholdt, Aniruddha Shet
 - Fock matrix computations using producer-consumer parallelism
 - coupled model idioms (e.g., for use in CCSM)
 - Richard Barrett, Stephen Poole, Philip Roth
 - stencil idioms: 2D, 3D, sparse
 - sweep3D & wavefront-style computations
- In both cases...
 - ...great technical discussions and feedback
 - ...valuable sanity-check for language and implementation
 - ...studies comparing with Fortress, X10 forthcoming

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